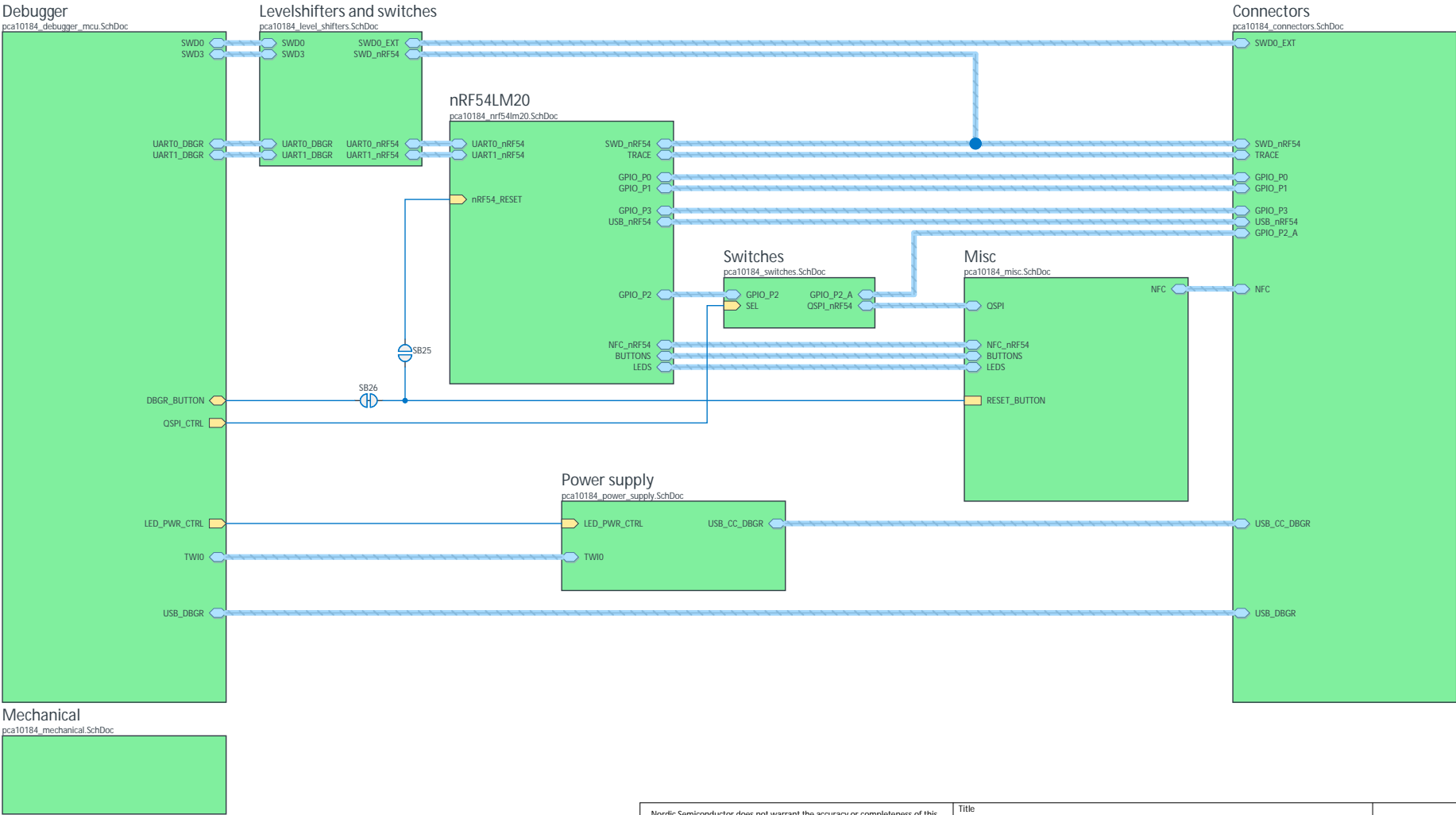


Nordic Semiconductor ASA

nRF54LM20-PDK (PCA10184)



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Title
nRF54LM20 Preview Development Kit

Size
A3

PCB Assembly Number
PCA10184

Date: 08.07.2025

File: pca10184_top_level.SchDoc

Classification: CONFIDENTIAL

Revision
0.3.4

Sheet 1 of 9

Drawn By: GETR



A

B

C

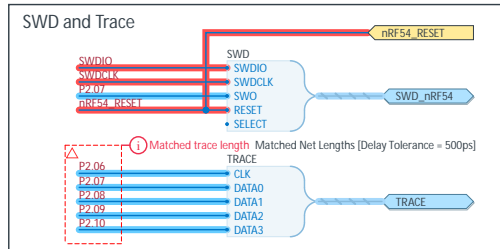
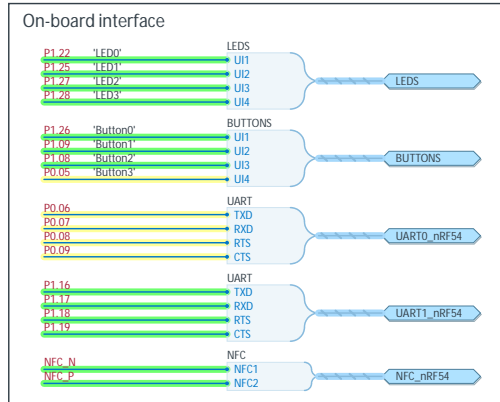
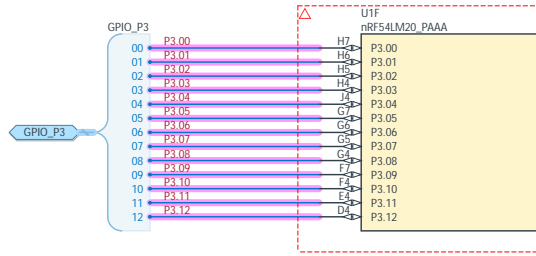
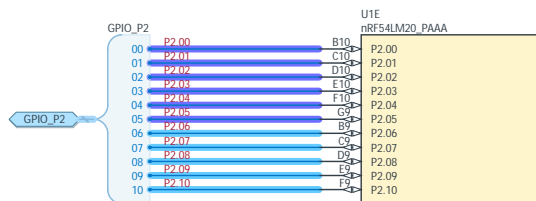
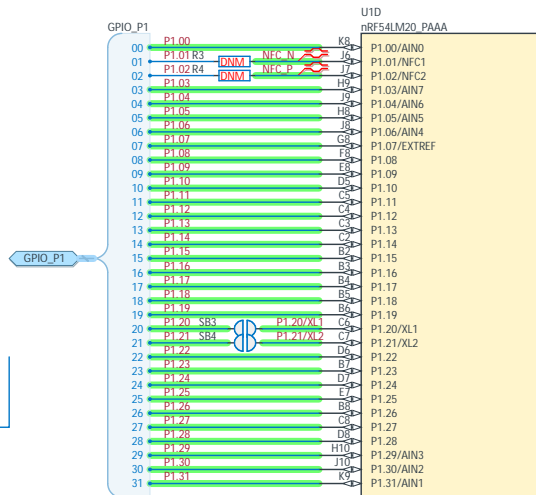
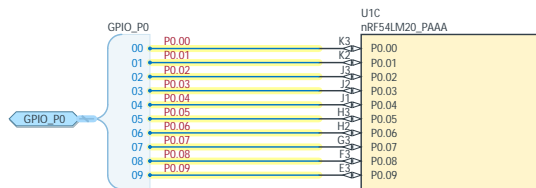
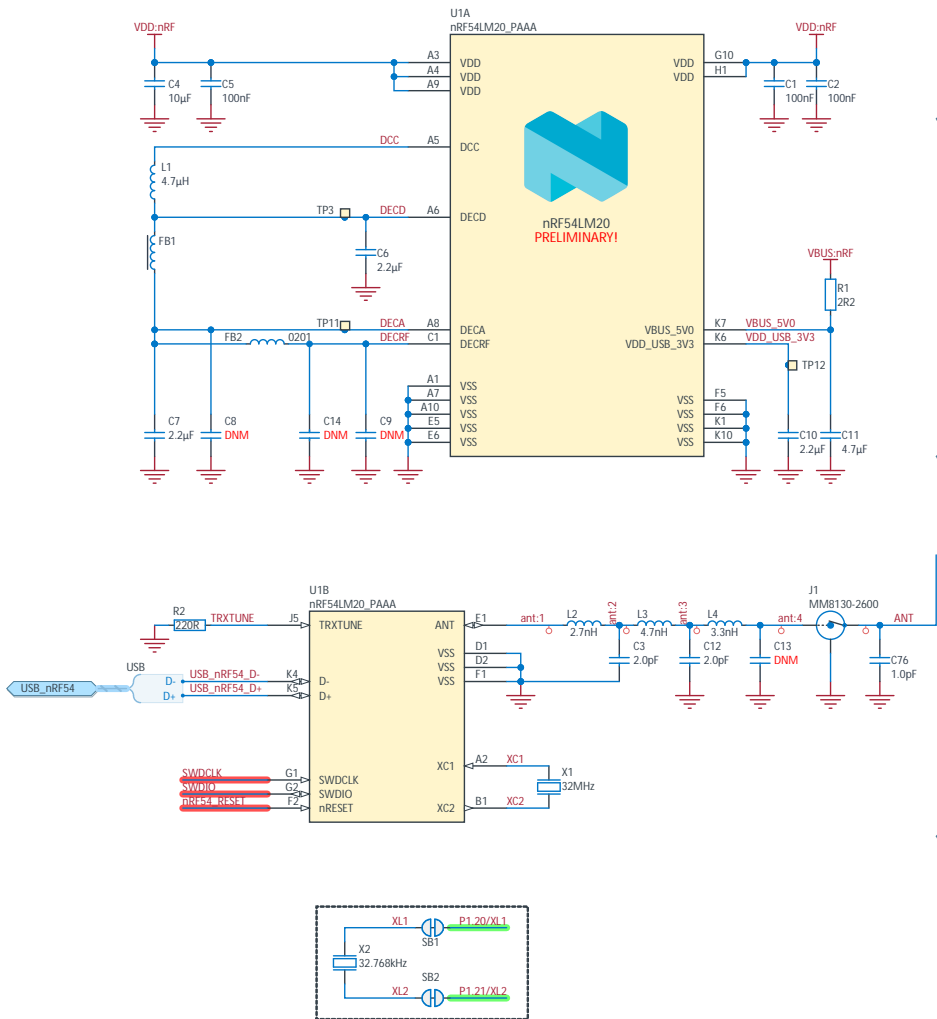
D

A

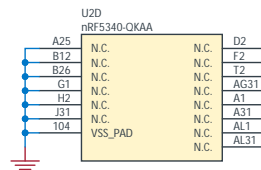
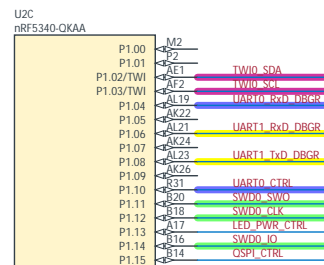
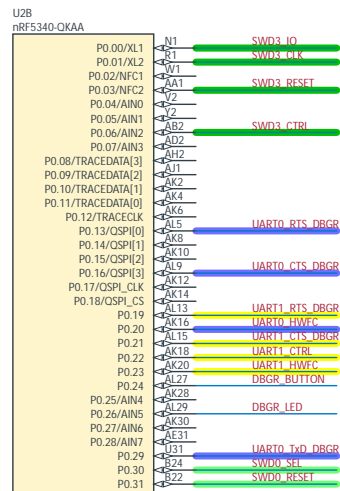
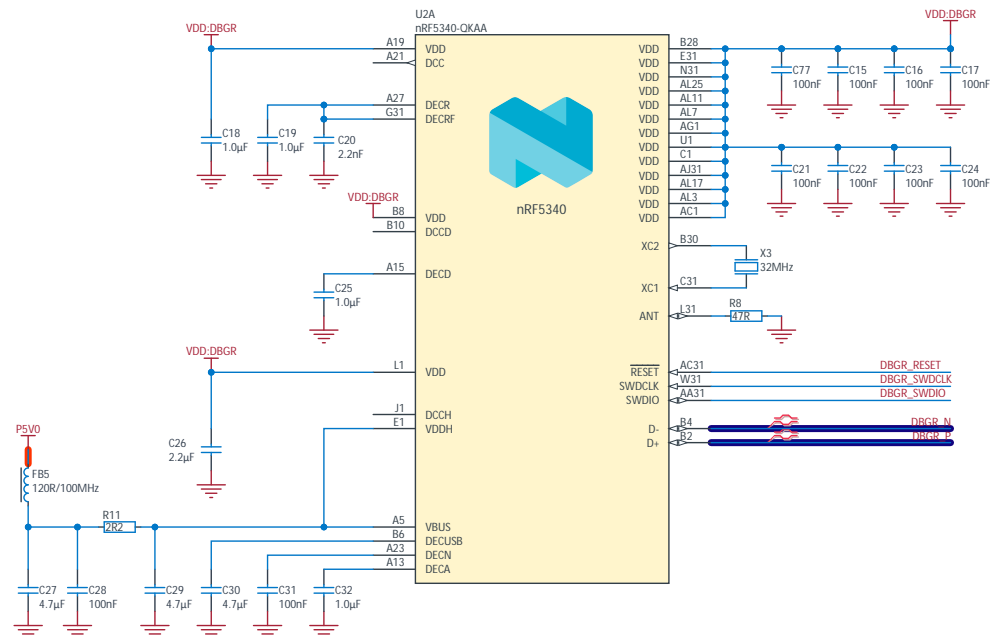
B

C

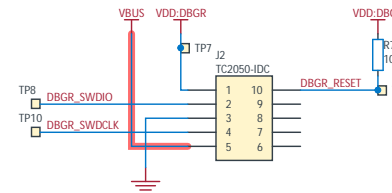
D



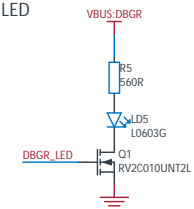
Debugger MCU



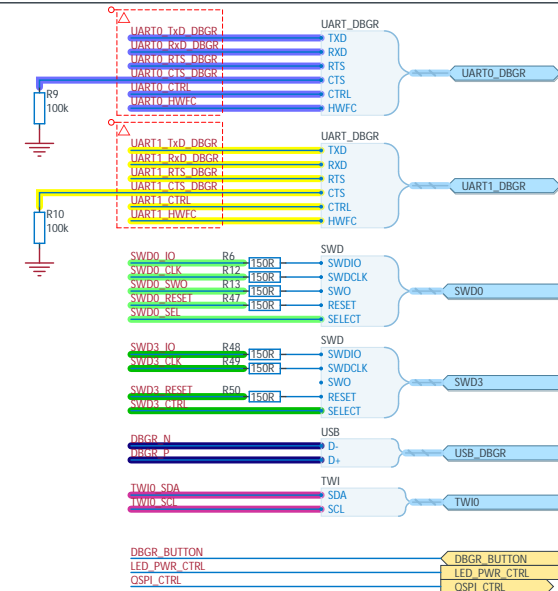
Debugger MCU Programming Connector



LED

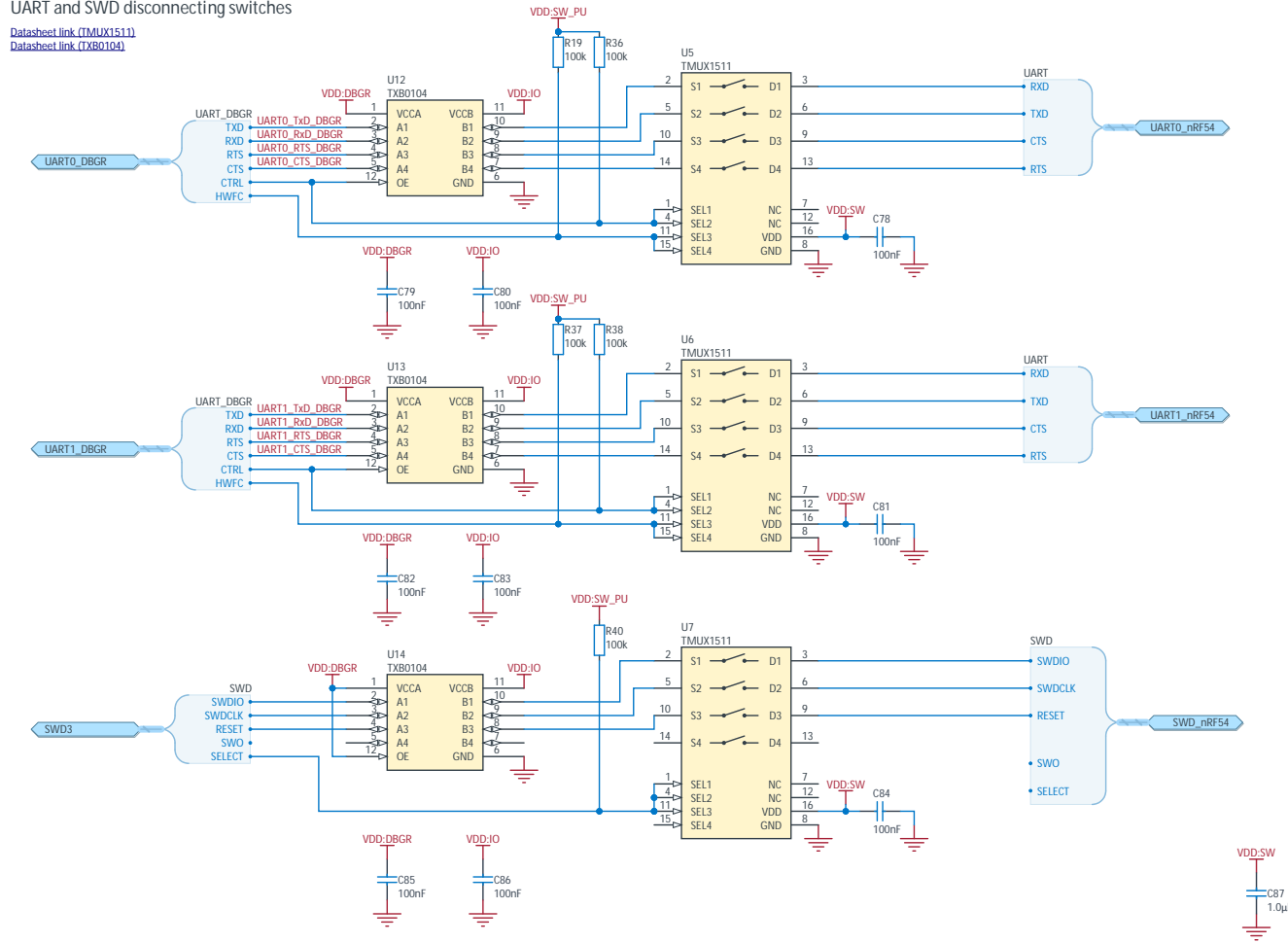


Ports

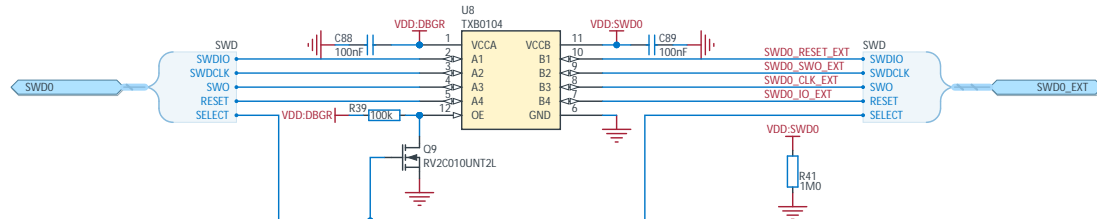


UART and SWD disconnecting switches

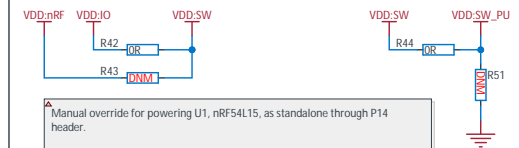
Datasheet link (TMUX1511)
Datasheet link (TXB0104)



SWD Debug Out level shifter



Analog switches manual override



Manual override for powering U1, nRF54L15, as standalone through P14 header.
R42, R44 mounted: Default configuration. DK requires power from USB connector.
R43, R51 mounted: Analog switches are powered from P14. Analog switches U5, U6, U7, U15 and U16 disconnects all signals. No external power needed from USB connector

Title
nRF54LM20 Preview Development Kit - Level Shifters

Size
A3

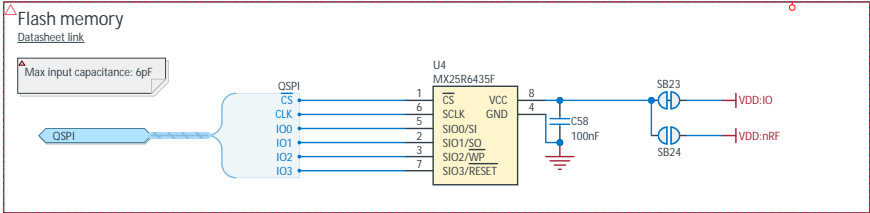
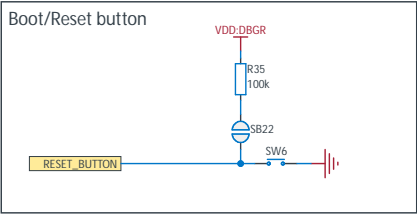
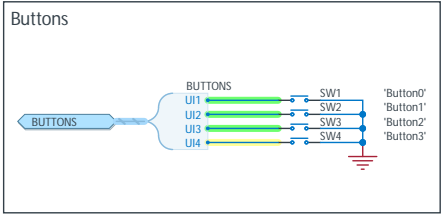
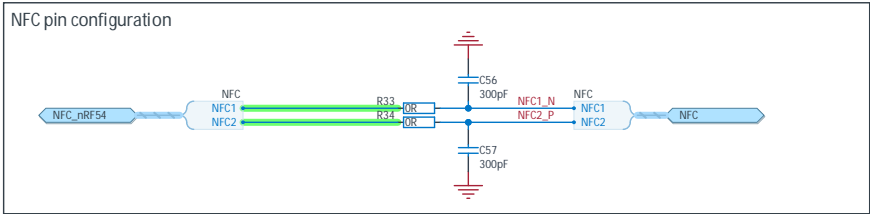
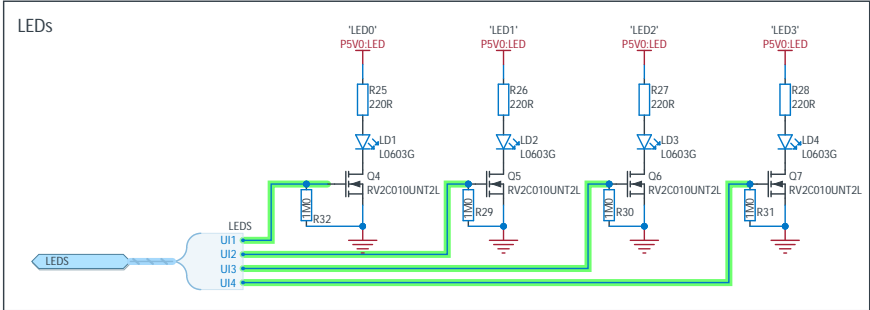
PCB Assembly Number
PCA10184

Date: 08.07.2025
File: pca10184_level_shifters.SchDoc
Classification: CONFIDENTIAL

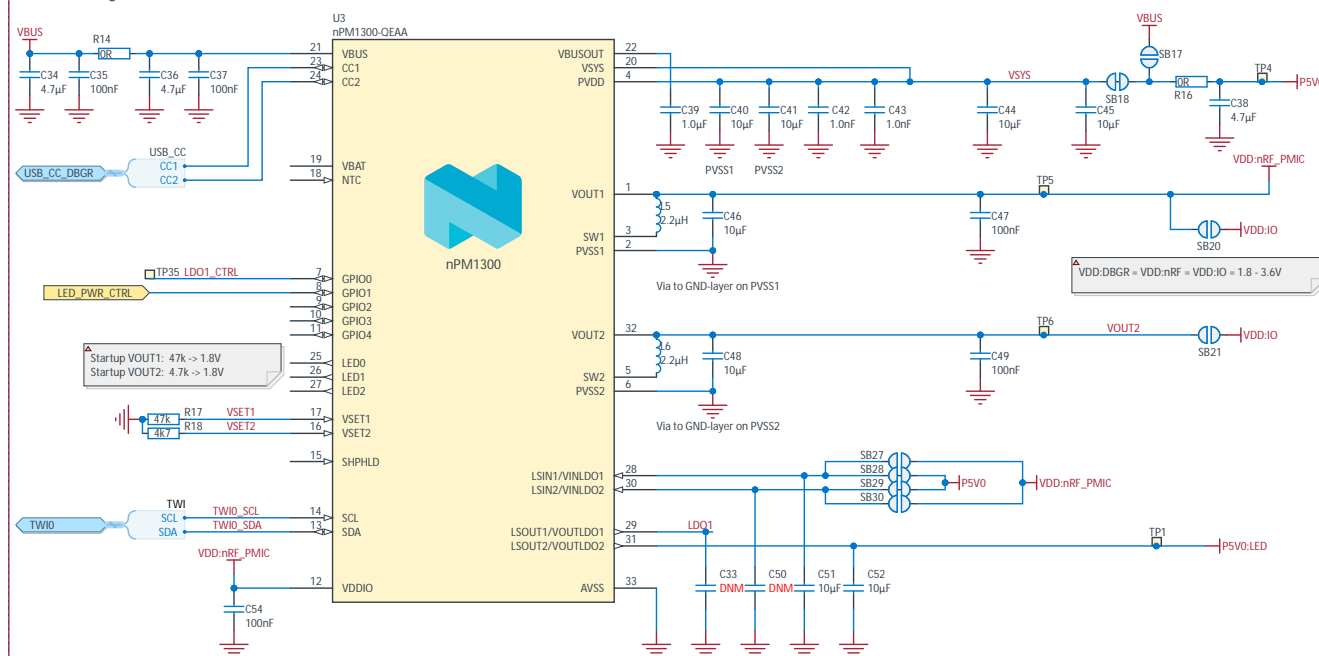
Revision
0.3.4

Sheet 4 of 9
Drawn By: GEIR

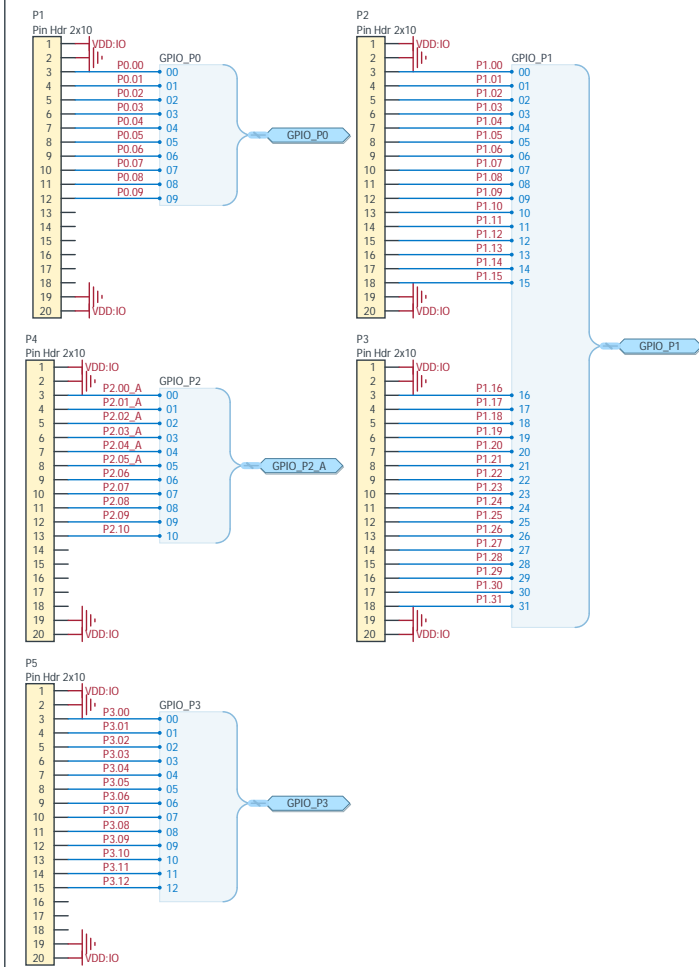




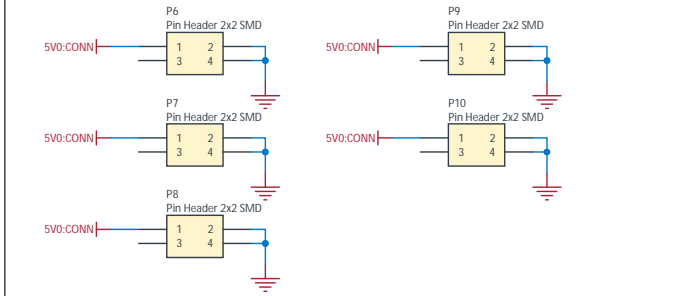
Power management



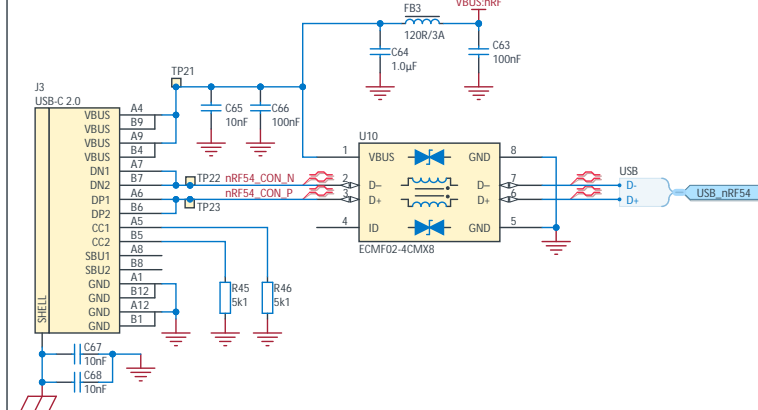
GPIO Connectors



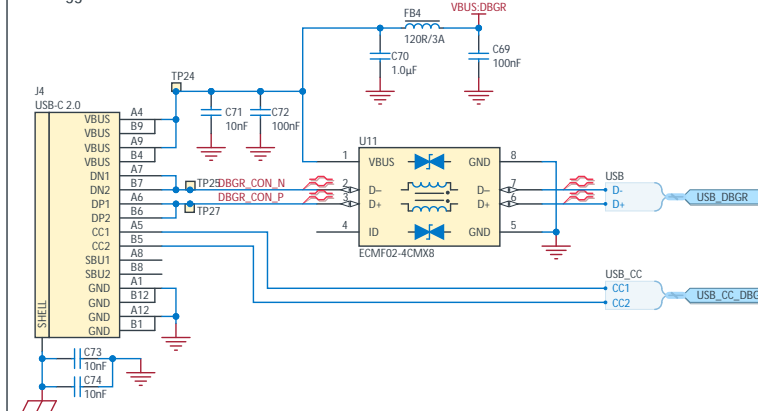
Power Connectors



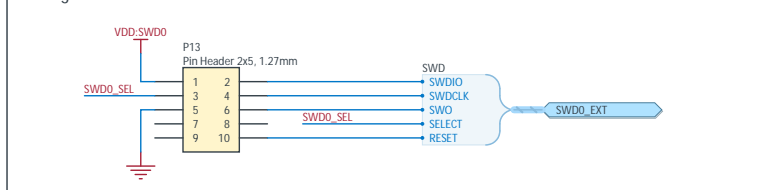
nRF USB Connector



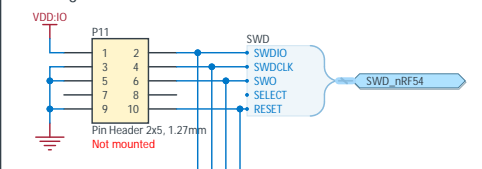
Debugger USB Connector



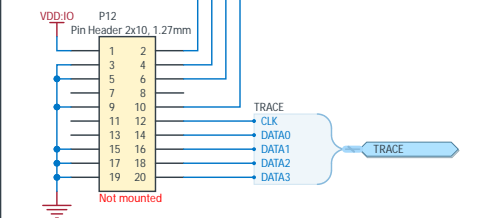
Debug OUT Connector



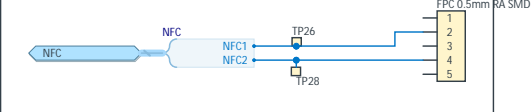
Debug IN Connector



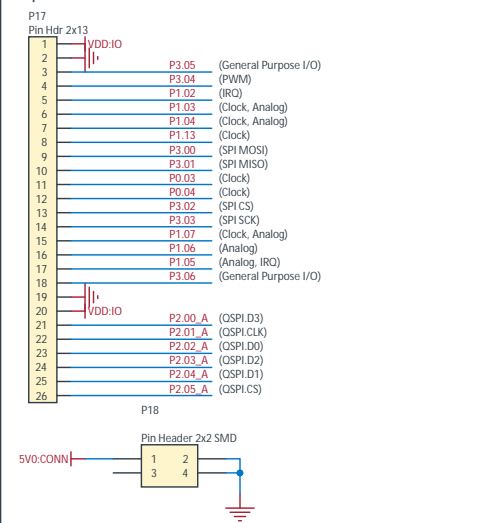
Trace connector



NFCT Antenna Connector



Expansion Board Header



Title
nRF54LM20 Preview Development Kit - Connectors

Size
A3

PCB Assembly Number
PCA10184

Date: 08.07.2025
File: pca10184_connectors.SchDoc
Classification: CONFIDENTIAL

Revision
0.3.4

Sheet 8 of 9
Drawn By: GEIR




The No ERC object is a design directive.
This directive is placed on a node in the circuit to suppress harmless warnings and/or error violation conditions that are detected when the schematic project is compiled.


Board fiducials



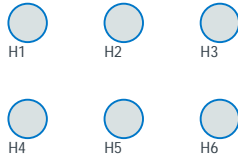
Logos and markings

PCA10184
0.3.4
<year>. <week>
<UID>


WEEE Symbol


Segger Logo S

Mechanical holes



Title		
nRF54LM20 Preview Development Kit - Mechanical		
Size	PCB Assembly Number	Revision
A4	PCA10184	0.3.4
Date: 05.06.2025		Sheet 9 of 9
File: pca10184_mechanical.SchDoc		Drawn By: GETR
Classification: CONFIDENTIAL		



